

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,016,089 B2  
APPLICATION NO. : 09/727486  
DATED : March 21, 2006  
INVENTOR(S) : Tomoya Yoneda et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

ON TITLE PAGE AT ITEM (54) TITLE

Line 1, "SOLID STATE" should read --SOLID-STATE--.

COLUMN 1

Line 1, "SOLID STATE" should read --SOLID-STATE--; and  
Line 37, "N" should read --an N--.

COLUMN 16

Line 62, "comprising," should read --comprising:--.

COLUMN 17

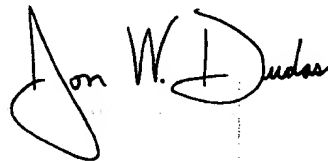
Line 36, "comprising," should read --comprising:--.

COLUMN 18

Line 5, "comprising," should read --comprising:--;  
Line 32, "wiring" should read --wiring,--;  
Line 53, "solid-state" should read --A solid-state--; and  
Line 54, "comprising," should read --comprising:--.

Signed and Sealed this

Twenty-seventh Day of March, 2007



JON W. DUDAS  
*Director of the United States Patent and Trademark Office*